

AMENDMENTS TO THE SPECIFICATION

**Please amend paragraph [0041] as follows:**

**[0041]** To effectively use this battery information, an FPGA can include a controller. Figure 2 illustrates a simplified FPGA 200 including a battery controller 201 in accordance with the invention. FPGA 200 further includes a standard power supply VCC pin 204 and a dedicated, battery voltage VBATT pin 205. Battery controller 201 can control a battery (primary or secondary) external to FGPA 200 via VBATT pin 205 (note that FPGA 200 could refer to an FPGA IC or a packaged FPGA IC). Because of critical circuits 207, e.g. ~~the previously described~~ a BRAM for providing information regarding one or more batteries, decrypting an encrypted bitstream, or any other circuits providing critical functions, a continuous power supply is required. In this embodiment, the power can be supplied either from VCC pin 204 or VBATT pin 205. Note that VCC pin 204 is directly connected to critical circuits 207, whereas VBATT pin 205 is selectively connected to critical circuits 207 via a switch 209.